

Seat No.

All Questions are *compulsory*.



12223

Instructions :

3 Hours / 70 Marks

(1)

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- (b) Draw a single stage transistor amplifier with voltage series negative feedback. Write its effect on voltage gain, input resistance, output resistance and harmonic distortion.
- (c) Sketch the circuit diagram of Dual Voltage Regulator using IC 78XX and 79XX to obtain ± 12 V output voltage.
- (d) Describe working of RC phase shift oscillator with neat sketch. Write formula for frequency of oscillation.

3. Attempt any THREE of the following :

- (a) Compare different types of power amplifier on the basis of
 - (i) Efficiency
 - (ii) Power dissipation in transistor
 - (iii) Conduction angle of collector current
 - (iv) Position of Q point.
- (b) Explain with circuit diagram the working of Class B push pull amplifier.
- (c) (i) State types of LC and RC oscillators.
 - (ii) Compare LC and RC oscillators on any four points.
- (d) State the necessity of regulated power supply. Define Load and Line regulation.

4. Attempt any THREE of the following :

- (a) Compare small signal amplifier and power amplifier (any four points).
- (b) Draw a neat labelled diagram of a two stage RC coupled amplifier. Draw its frequency response. State its two advantages.
- (c) Draw the block diagram of voltage series and current series feedback.
- (d) Draw Miller sweep generator and give its application.
- (e) Draw the high voltage regulator using IC723 and explain its operation in brief.

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5. Attempt any TWO of the following :

- (a) Draw the circuit diagram of crystal oscillator. Give basic principle of piezoelectric crystal. Give advantages of crystal oscillator.
- (b) Compare class A, class B, class AB and class C with efficiency, conduction angle, Q point location and distortion.
- (c) Draw common source FET amplifier. Describe its operation. Give its application.

6. Attempt any TWO of the following :

- (a) Draw circuit diagram of single tuned and double tuned amplifier. Compare single tuned and double tuned amplifier on the basis of (i) Selectivity (ii) Q-factor (iii) Bandwidth (iv) Response of gain Vs frequency.
- (b) In single stage voltage amplifier, voltage gain without feedback is 80, input resistance $Ri = 800 \Omega$ and output resistance $Ro = 8k \Omega$. If 20% output voltage is feedback in series with input, determine Avf, Rif, Rof of the negative feedback amplifier.
- (c) Sketch the complementary symmetry push pull amplifier and explain working with waveform.

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