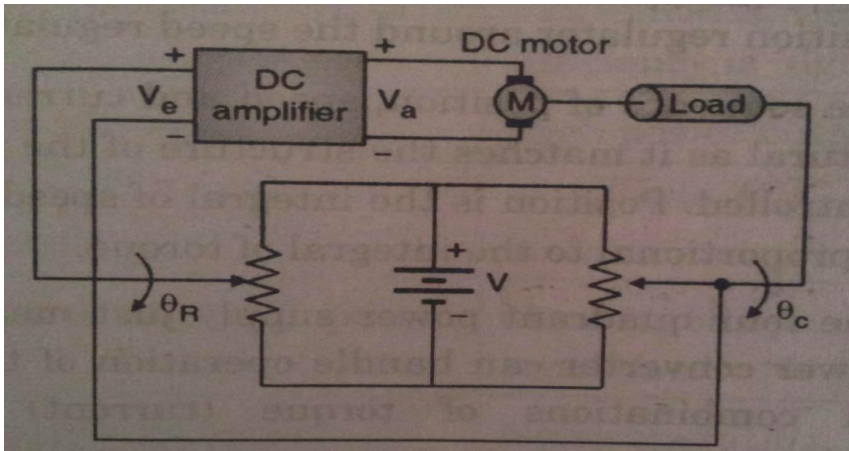
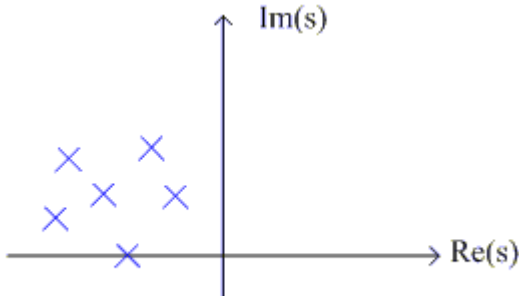
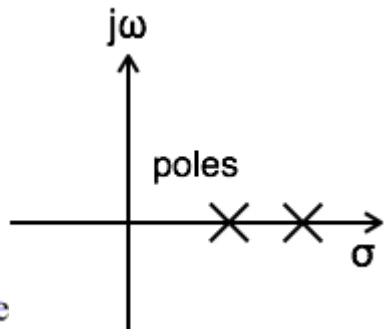


**Important Instructions to examiners:**

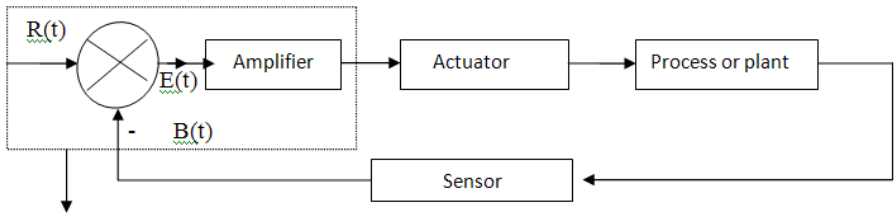
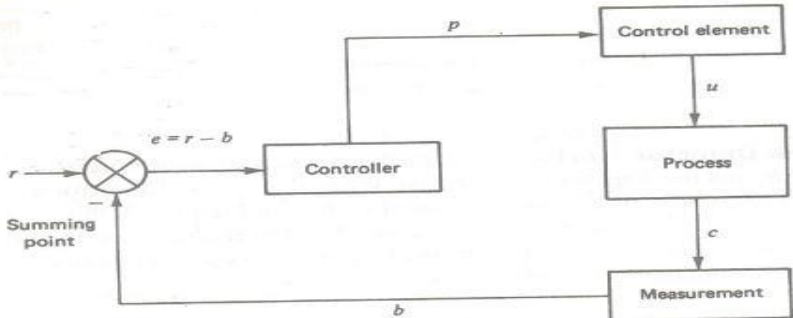
- 1) The answers should be examined by keywords and not as word-to-word as given in the model answer scheme.
- 2) The model answer and the answer written by candidate may vary but the examiner may try to assess the understanding level of the candidate.
- 3) The language errors such as grammatical, spelling errors should not be given more Importance. (Not applicable for subject English and Communication Skills.)
- 4) While assessing figures, examiner may give credit for principal components indicated in the figure. The figures drawn by candidate and model answer may vary. The examiner may give credit for any equivalent figure drawn.
- 5) Credits may be given step wise for numerical problems. In some cases, the assumed constant values may vary and there may be some difference in the candidate's answers and model answer.
- 6) In case of some questions credit may be given by judgement on part of examiner of relevant answer based on candidate's understanding.
- 7) For programming language papers, credit may be given to any other program based on equivalent concept.

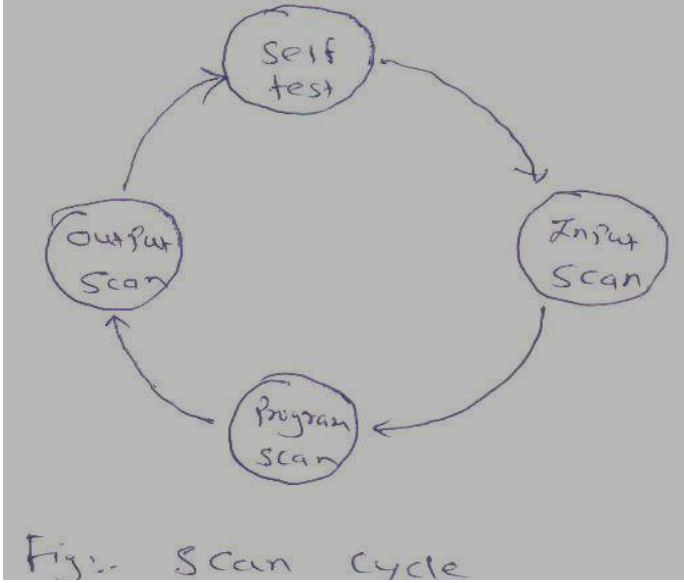
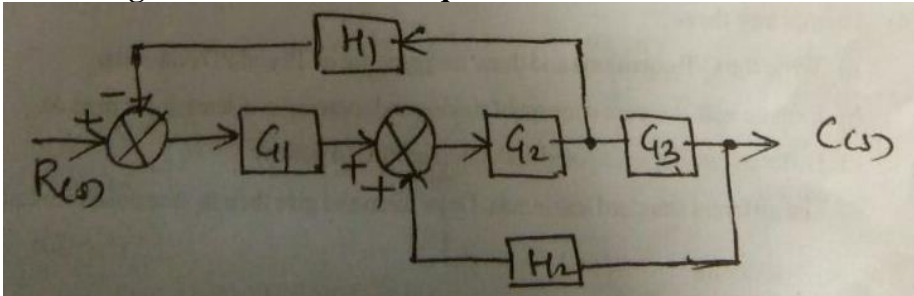
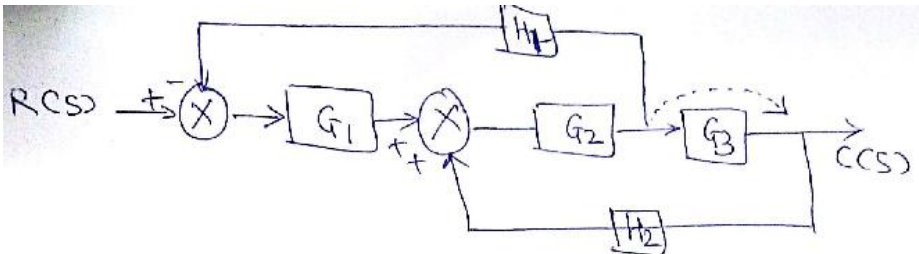
Q. No.	Question & its Answer	Remark	Total Marks
1 A)	Attempt any three:		12
a)	Draw the block diagram of DC Servo System.		04
Ans.	 <p style="text-align: center;">Fig: DC Servo Motor</p> <ol style="list-style-type: none"> 1) The servo system consists of error detector, amplifier, motor as controller and load whose position is to be changed. 2) DC servo system consists of potentiometer as a error detector, DC amplifier, DC motor, DC gear system and the DC load whose position is to be changed. <p style="text-align: center;">NOTE: Explanation is not compulsory</p>	03 Marks Diagram	01 Mark for neat Labeling

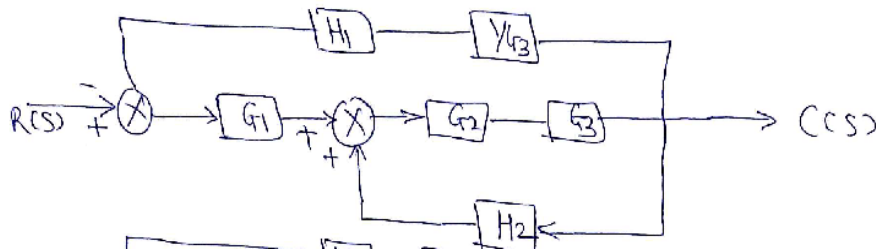


b)	State the need of PLC in automation.		04
Ans.	<p>Need of PLC in automation</p> <ul style="list-style-type: none"> To reduce human efforts. To get maximum efficiency from machine and control them with human logic To reduce complex circuitry of entire system To eliminate the high costs associated with inflexible, relay-controlled systems. Replacing Human Operators (Dangerous Environments & Beyond Human Capabilities) 	01 mark each (Any relevant four points)	
c)	Define stability and with the diagram of root location in s-plane define stable and unstable systems.		04
Ans.	<p>Stability: The system is said to be stable if it produces bounded output for a bounded input. It is used to define usefulness of the system. The stability implies that the system performance should not change even if there are small changes in system input. Any control system must be stable.</p> <div style="display: flex; justify-content: space-around; align-items: flex-end;"> <div style="text-align: center;">  <p>All roots are in the left half of the plane</p> <p>Stable System</p> </div> <div style="text-align: center;">  <p>Unstable System</p> </div> </div> <ul style="list-style-type: none"> The system is said to be stable if poles of closed loop the system lies on left half of s-plane The system is said to be unstable if poles of closed loop system lies on right half of s-plane <p style="text-align: center;">OR</p> <ul style="list-style-type: none"> STABILITY: A linear time invariant system is said to be stable if the system is excited by a bounded input, output is also bounded and controllable. In the absence of the input, output must tend to zero irrespective of the initial condition. UNSTABLE: A linear time invariant system is said to be unstable if for a bonded input it produces unbounded output. In absence of the input, output may not return to zero it shows certain output without input. 	<p>01 mark Stability</p> <p>01 Mark Diagram of root location</p> <p>01 Mark Stable System</p> <p>01 Mark Unstable System</p>	

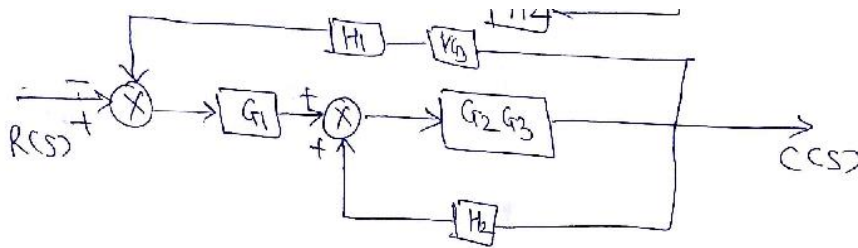


d)	Draw block diagram of Process Control System.		04
Ans.	 <p>Automatic controller</p> <p>Process control system consists of process or plant , sensor, error detector, automatic Controller, actuator or control element.</p> <p style="text-align: center;">OR</p>  <p>The block diagram of process control system consists of Measuring element, Error detector, Controller, Final control element and Process.</p>	03 Marks Diagram	
B)	Attempt any ONE :		06
a)	<p>Explain:</p> <p>i) Benefits of PLC in automation(3 points)</p> <p>ii) Scanning Cycle</p>		06
Ans.	<p>i) Benefits of PLC in automation</p> <ul style="list-style-type: none"> • Higher productivity. • Superior quality of end product. • Efficient usage of energy and raw materials • Improved safety in working conditions. • Fast • Easily programmed and have an easily understood programming language. <p>ii) Scanning Cycle</p> <ul style="list-style-type: none"> • It is number of states/steps which the controller follows when it is put in RUN mode. • It is also called as operating cycle and is defined as “the number of states through which the controller scan the program before execution” • The loaded program is kept in memory of PLC and every time the 	03 Mark (Any three points)	01 Mark Definition

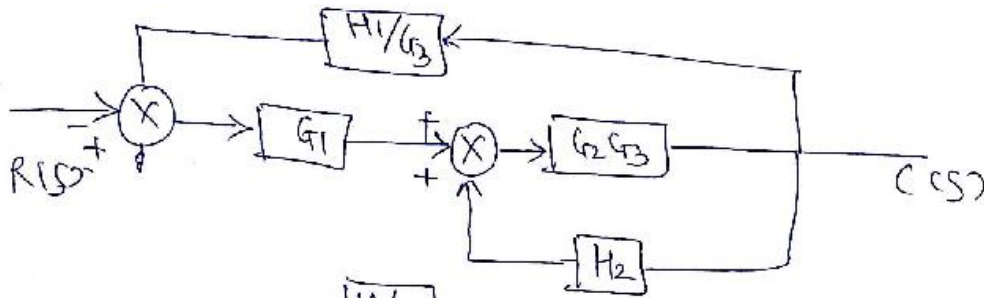
	<p>program will be scan by the PLC. It has four states which are shown in fig. below.</p>  <p>Fig. Scan Cycle</p> <ul style="list-style-type: none"> The significance of scan cycle in PLC is to test the program and make it error free by going through above four states i.e. self test, input scan, program scan and output scan. 	<p>01 Mark Diagram</p> <p>01 Mark (Significance or need)</p>	
<p>b)</p>	<p>Derive the transfer function of the system as shown in figure 1, using block diagram reduction techniques.</p>  <p>Figure 1</p>		<p>06</p>
<p>Ans.</p>	<p>Shift Take Off point after G3 block, we get</p> 	<p>01 Mark</p>	



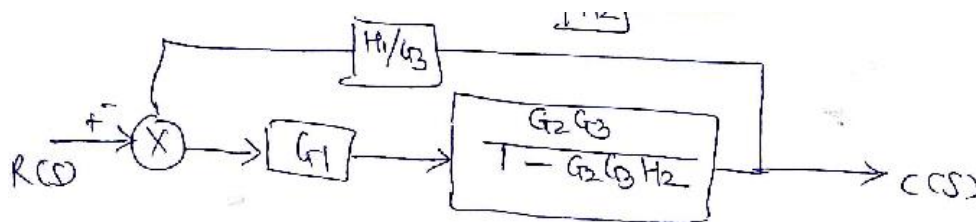
01 Mark

Multiplying G_2 & G_3 , We get

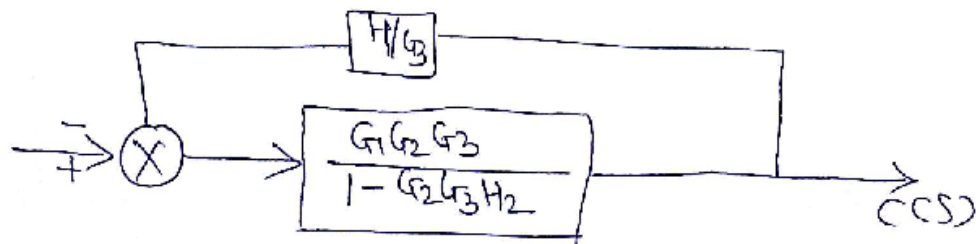
01 Mark

Multiplying H_1 & $1/G_3$, we get

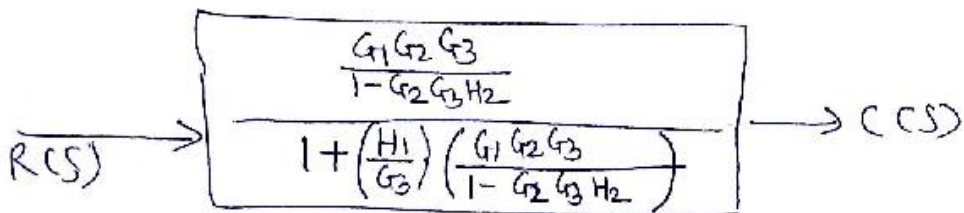
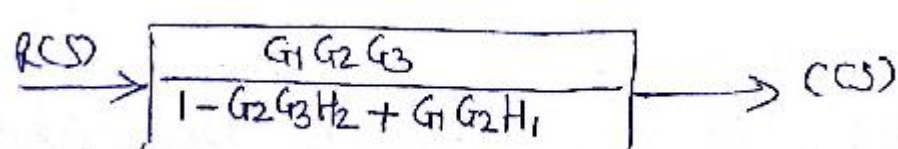
01 Mark

Eliminating Feedback loop of H_2 , we get

01 Mark

Multiplying G_1 and other transfer function, we get

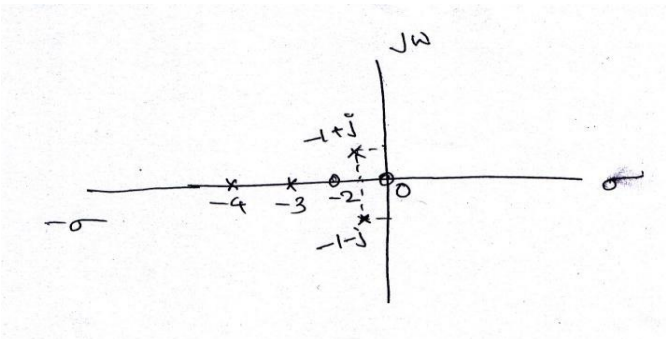


	<p>Eliminating feedback loop H_1 / G_3 we get,</p>  <p>Final Transfer function can be obtained from following diagram</p> 	01 Mark	
2	Attempt any TWO:		16
a)	<p>A) A second order system is given</p> $\frac{C(s)}{R(s)} = \frac{6}{s^2 + 5s + 6}$ <p>Determine :</p> <p>a) Rise Time b) Peak Time c) Settling Time d) Peak overshoot</p>		08
Ans.	<p>Comparing equation 1 with standard equation,</p> $\frac{C(s)}{R(s)} = \frac{W_n^2}{s^2 + 2 \cdot \xi \cdot W_n \cdot s + W_n^2}$ <p>We get,</p> <p>$W_n^2 = 6,$ So, $W_n = 2.45 \text{ rad /s}$</p> <p>$2 \cdot \xi \cdot W_n = 5$ So, $\xi = 1.02 \text{ rad / s (approx.. 1 rad/s)}$</p> <p>$W_d = W_n \sqrt{1 - \xi^2}$ So, $W_d = 0 \text{ rad /s}$</p> <p>Assume $\xi = 0.8$ (or less than 1) and find w_d, T_r, T_p, T_s & $\%M_p$.</p> <p>So that system is underdamped we find all parameters.</p>	<p>01 Mark</p> <p>01 Mark</p> <p>01 Mark</p> <p>01 Mark</p>	

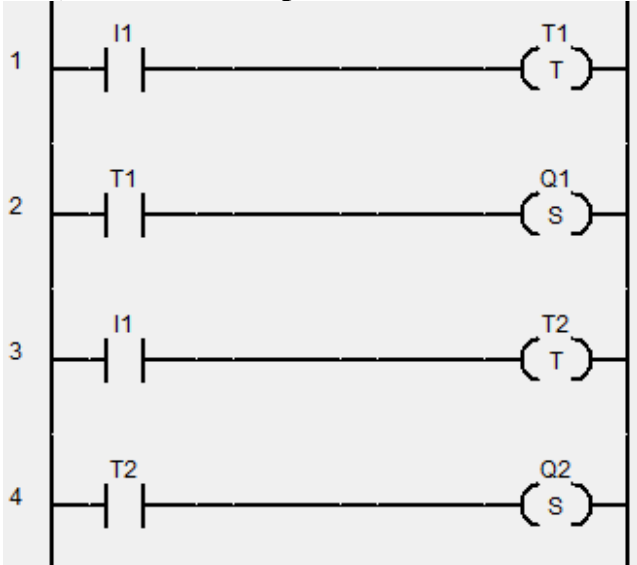
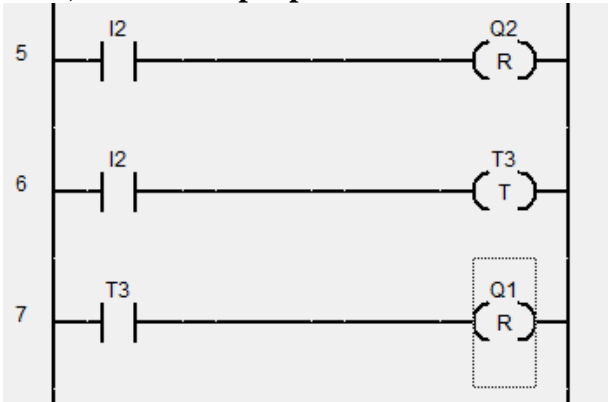


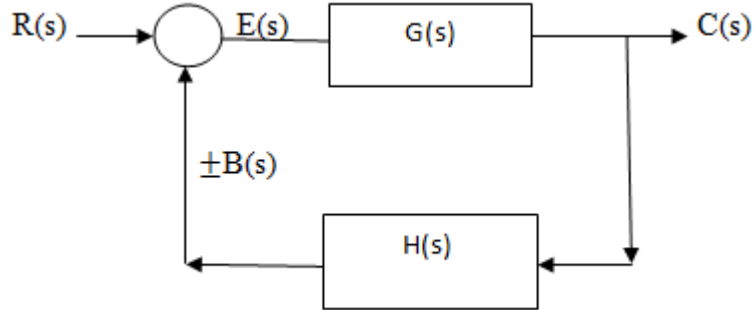
	<p style="text-align: center;"><u>OR</u></p> <p>Ideally the above 4 listed parameters can be given as,</p> <p>i) Rise time is given by $t_r = \frac{\pi - \beta}{\omega_d}$,</p> <p style="text-align: center;">Where $\beta = \frac{\sqrt{1 - \zeta^2}}{\zeta}$</p> <p>ii) Peak Time is given by $t_p = \frac{\pi}{\omega_d}$</p> <p>iii) Max overshoot is given by $M_p\% = 100 \times e^{-\frac{\pi\zeta}{\sqrt{1 - \zeta^2}}}$</p> <p>iv) Settling time is given by $t_s = \frac{4}{\zeta \cdot \omega_n}$</p> <p>System is critically damped & hence no oscillations and no damped Therefore all 4 specifications do not exist on the response of the above system.</p> <p>NOTE: Any appropriate answer with formula and suitable assumption may also considered. If the problem is solved by assuming any arbitrary value of zeta approximately near to 1 then also 02 marks may be given</p>	<p>02 Mark Formulae</p> <p>02 Mark (Conclusion or calculation with assumed value of zeta)</p>	
b)	<p>For a given transfer function</p> $\frac{C(s)}{R(s)} = \frac{s(s+2)}{(s^2+2s+2)(s^2+7s+12)}$ <p>Find:</p> <p>i) Poles ii) Zeros iii) Pole-Zero Plot iv) Characteristic Equation</p>		08
Ans.	<p>i) Poles: We can get poles from equations in the denominator</p> <p>1) $(s^2 + 7s + 12) = 0$.</p> <p>So root of the equation can be determined as</p> <p>$(s+3).(s+7)=0$ i.e. either $s = -3$ OR $s = -4$</p>	<p>01 Mark</p>	



	<p>2) $(s^2 + 2s + 2)$</p> <p>For the quadratic equation $ax^2+bx+c=0$,</p> <p>the poles are = $\frac{-b \pm \sqrt{b^2 - 4ac}}{2a} = \frac{-2 \pm \sqrt{2^2 - 4 \cdot 1 \cdot 2}}{2 \cdot 1} =$</p> <p>i.e. - $\frac{-2 \pm \sqrt{-4}}{2} = \frac{-2 \pm 2j}{2} = -1 \pm j$</p> <p>Therefore poles are $-1+j$ & $-1-j$</p> <p>ii)Zeros: We can get zeros from equation in the numerator</p> <p>So for $s(s+2)$ equation we can get roots by comparing it with zero</p> <p>$s(s+2) = 0$</p> <p>So zeros i.e. roots of the equation are 0 & -2</p> <p>iii)Pole-Zero Plot:</p>  <p>iv)Characteristic equation= $(s^2 + 2s + 2)(s^2 + 7s + 12)$</p>	<p>02 Mark</p> <p>02 Mark</p> <p>02 Mark</p> <p>01 Mark</p>	
<p>c)</p>	<p>Draw ladder diagram for 2 motor operation for following condition</p> <p>i) Start push button starts motor M_1 after 10 seconds and motor M_2 after 20 seconds.</p> <p>ii) When stop push button is pressed it stops M_2 and after 15 seconds M_1.</p>		<p>08</p>
<p>Ans.</p>	<p>I1 & I2 are start & stop push buttons.</p> <p>T1 is On Delay Timer which turns on after 10 seconds after getting I1</p> <p>T2 is On Delay Timer which turns on after 20 seconds after getting I1</p> <p>T3 is On Delay Timer which turns on after 10 seconds after getting I2</p>		



	<p>i) Motor start operation</p>  <p>ii) Motor Stop Operation</p>  <p>NOTE: Any relevant ladder logic may considered.</p>	<p>04 Mark</p>	
3	Attempt any FOUR :		16
a)	Define transfer function. Derive an expression for transfer function of closed loop system.		04
Ans.	<p>Definition-Transfer function is the ratio of Laplace transform of output of system to Laplace transform of input of system ,when all initial conditions are assumed to be zero.</p> <p>Expression for Transfer function of closed loop system</p> <p>Consider a simple form of closed loop system</p>	<p>01 Mark</p> <p>Definition</p>	



Where

$R(s)$ -Reference input

$G(s)$ -Forward path T.F

$C(s)$ - Controlled output

$H(s)$ –Feedback path T.F

$E(s)$ -Error signal

$B(s)$ -Feedback signal

According to T.F Definition

$$\text{Transfer Function} = \frac{\text{Laplace of output}}{\text{Laplace of input}} = \frac{C(s)}{R(s)}$$

Here Error signal is given by

$$E(s) = R(s) \pm B(s) \dots \dots \dots (1)$$

Feedback signal is given below

$$B(s) = C(s).H(s) \dots \dots \dots (2)$$

Output of system is given as

$$C(s) = G(s).E(s) \dots \dots \dots (3)$$

Put the value of $E(s)$ and $B(s)$ in equation (3)

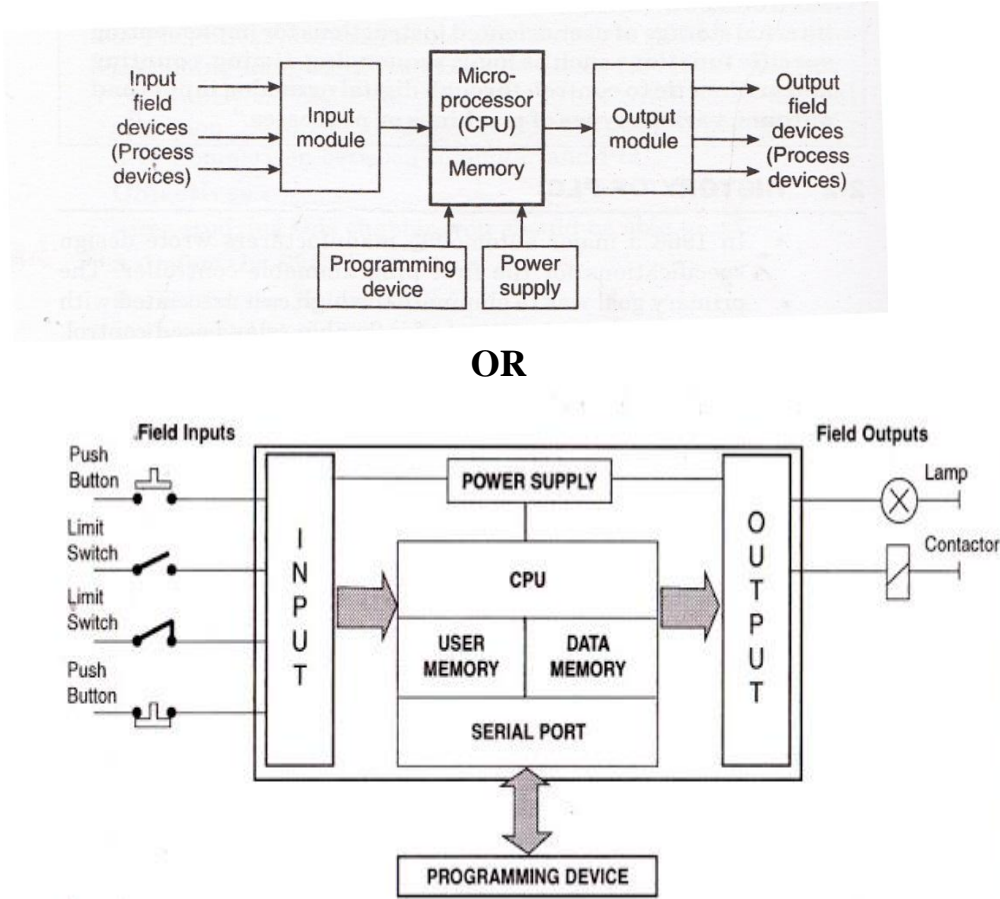
$$C(s) = G(s).[R(s) \pm C(s).H(s)]$$

$$C(s) = G(s).R(s) \pm G(s)C(s).H(s)$$

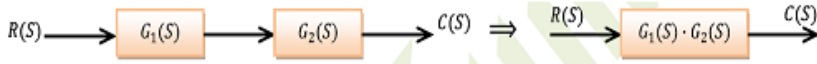
$$C(s) \mp G(s)C(s).H(s) = G(s).R(s)$$

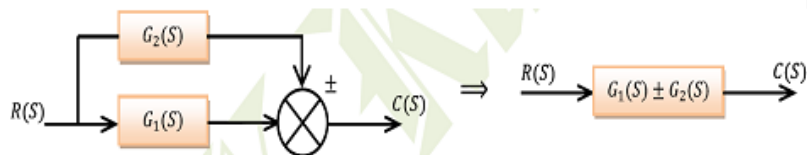
$$C(s)[1 \mp G(s)H(s)] = G(s)R(s)$$

**03 Mark
Expression**

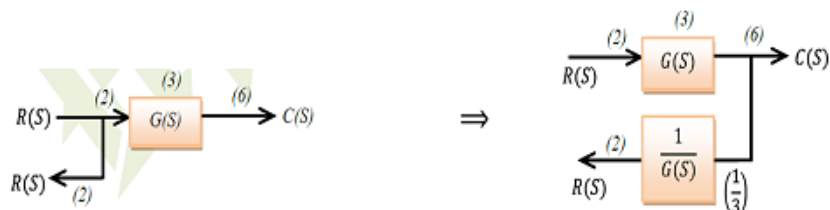
	<p>By rearranging the equation ,we get Transfer Function of closed loop system as</p> $\frac{c(s)}{R(s)} = \frac{G(s)}{[1 + G(s)H(s)]}$		
b)	Draw the block diagram of PLC and explain its C.P.U. block.		04
Ans.	 <p>A simplified block diagram of a PLC shown in above Fig. It has three major units/sections.</p> <ul style="list-style-type: none"> • I/O (Input/Output) Modules. • CPU (Central Processing Units). • Programmer/Monitor. <p>CPU Section:-</p> <p>The Central Processing Unit, the brain of the system is the control portion of the PLC. It has three Subparts.</p>	<p>02 Marks Block Diagram</p> <p>02 Marks for relevant Explanation</p>	



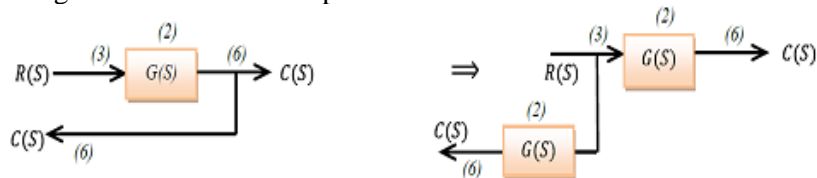
	<ul style="list-style-type: none"> Memory System Processor Power Supply <p>Memory System:-</p> <p>The memory is the area of the CPU in which data and information is stored and retrieved. The total memory area can be subdivided into the following four Sections.</p> <ul style="list-style-type: none"> I/O Image Memory <p>The input image memory consists of memory locations used to hold the ON or OFF states of each input field devices, in the input status file.</p> <p>The output status file consists of memory locations that stores the ON or OFF states of hardware output devices in the field. Data is stored in the output status file as a result of solving user program and is waiting to be transferred to the output module's switching device.</p> <ul style="list-style-type: none"> Data Memory <p>It is used to store numerical data required in math calculation, bar code data etc.</p> <ul style="list-style-type: none"> User Memory <p>It contains user's application program.</p> <ul style="list-style-type: none"> Executive Memory <p>It is used to store an executive program or system software. An operating system of the PLC is a special program that controls the action of CPU and consequently the execution of the user's program. A PLC operating system is designed to scan image memory, interprets the instruction of user's program stored in main memory, and executes the user's application program the operating system is supplied by the PLC manufacturer and is permanently held in memory.</p>		
c)	State with diagram any four block diagram reduction rules.		04
Ans.	<p>i) Combining a block in cascade: When two or more blocks are connected in series, their overall transfer function is the product of individual block transfer function.</p>  <p>ii) Combining two blocks in parallel: When two or more blocks are connected in parallel, their overall transfer function is the addition or difference of individual transfer function.</p>	04 Marks (Any four rules)	



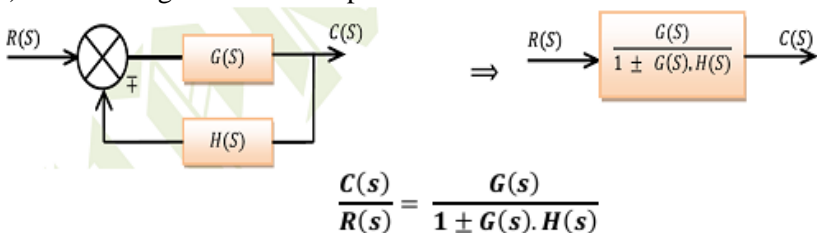
iii) Shifting a take off point after a block: To shift take off point after a block, we shall add a block with transfer function $1/G$ in series with signal having taking off from that point.



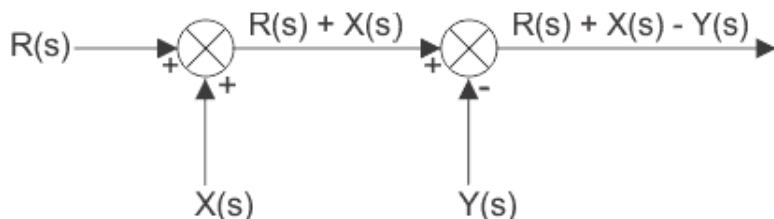
iv) Shifting a take off point before a block: To shift take off point before a block, we shall add a block with transfer function G in series with signal having taking off from the take off point



v) Eliminating Feedback Loop:





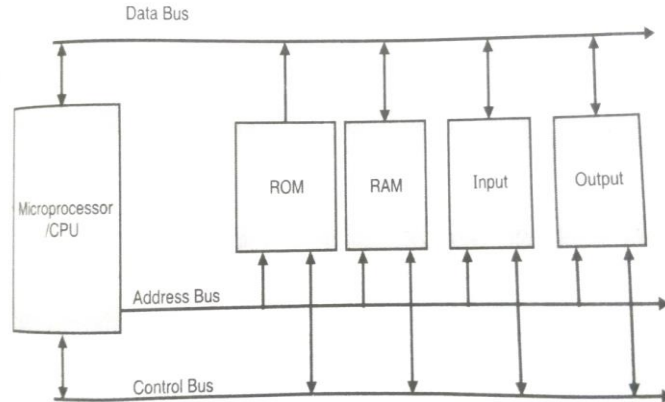
vi) Interchanging Summing Points: The order of summing points can be interchanged, if two or more summing points are in series and output remains the same.



vii) Moving Take off point before a summing point: To shift a take off point before summing point, add a summing point in series with take off point.



	 <p>viii) Moving Take off point after a summing point: To shift a take off point after summing point, one more summing point is added in series with take off point.</p>  <p>ix) Moving summing point after a block: To shift summing point after a block, another block having transfer function G is added before the summing point. x) Moving summing point before a block: To shift summing point before a block, another block having transfer function $1/G$ is added before the summing point.</p>																						
d)	By means of Routh's criteria determine the stability of the system $s^4 + 2s^3 + 8s^2 + 4s + 3 = 0$.		04																				
Ans.	<p>Find even & odd coefficient from characteristics equation</p> $F(s) = s^4 + 2s^3 + 8s^2 + 4s + 3 = 0$ <p>(2) Makes Routh's array</p> <table border="1" data-bbox="397 1239 828 1585"> <tr> <td>S4</td><td>1</td><td>8</td><td>3</td></tr> <tr> <td>S3</td><td>2</td><td>4</td><td>0</td></tr> <tr> <td>S2</td><td>6</td><td>3</td><td>0</td></tr> <tr> <td>S1</td><td>3</td><td>0</td><td></td></tr> <tr> <td>S0</td><td>3</td><td>0</td><td></td></tr> </table> <p>3) Conclusion – As in the first column of Routh's array there is no sign change in the first column therefore system is stable</p>	S4	1	8	3	S3	2	4	0	S2	6	3	0	S1	3	0		S0	3	0		<p>03 Marks Routh array</p> <p>01 Mark conclusion</p>	
S4	1	8	3																				
S3	2	4	0																				
S2	6	3	0																				
S1	3	0																					
S0	3	0																					
e)	Explain the function and Organization of memory in PLC.		04																				
Ans.	Organization of memory in PLC																						



**02 Marks
Diagram**

Different types of memory that generally used in PLC are as follows

1. Random Access memory-i)NOVRAM
2. Read only memory-i)PROM ii)EPROM iii)EEPROM

1. Random Access memory-RAM is volatile memory means as the power is lost, it's memory erased. But if CPU has battery backup, the information in RAM can not be erased. RAM memory is used to save input data and output information.

NOVRAM- It is one of the type of RAM. NOVRAM is the combination of EEPROM and RAM. When power is go off, the contents of RAM memory are quickly stored in the EEPROM. And the stored data can be read from RAM when power is again restored.

2. ROM-It is non volatile memory, and used for storing users program so that the program can retain during power failure.

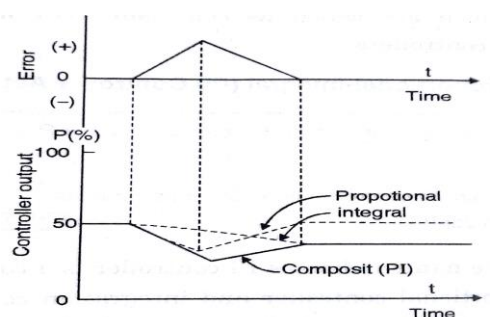
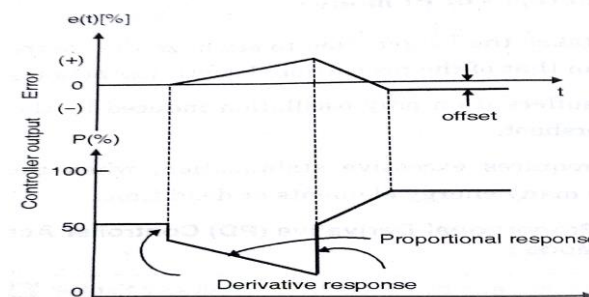
i) PROM-It is the type of ROM .It is similar to ROM except that it may be programmed once and once only by the user. To change the program in a programmed PROM, throw it away and replace it with a new unprogrammed PROM.

ii) EPROM- It is the type of ROM. The erasable programmable read-only memory (EPROM) is a PROM that can be erased. The data in the EPROM can be erased by focusing the UV light for a few minutes on the top of EPROM. Thus it is also called as UVPROM.

iii) EEPROM- It is the type of ROM. The electrically erasable

**02 Marks
explanation**



	programmable read only memory is similar to the EPROM .Instead of UV light exposure for erasure, an electrical signal is applied to the chip. The speed of erasing of EEPROM is greater than EPROM.		
4 (A)	Attempt any THREE:		12
(a)	Write the O/P equations and draw the response of PI and PD controller.		04
Ans.	<p>1)PI controller-</p> <p>Output equation-</p> $P = K_p \cdot e_p + K_p K_i \int_0^t e_p(t). dt + P_I(0)$ <p>Response-</p> 	02 Marks O/P equations	02 Marks for Responses
	<p>2)PD controller-</p> <p>Output equation-</p> $P = K_p \cdot e_p + K_p K_D \frac{d}{dx}(e_p) + P_{(0)}$ <p>Response-</p> 		
(b)	Explain with diagram concept of sinking and sourcing in discrete input		04

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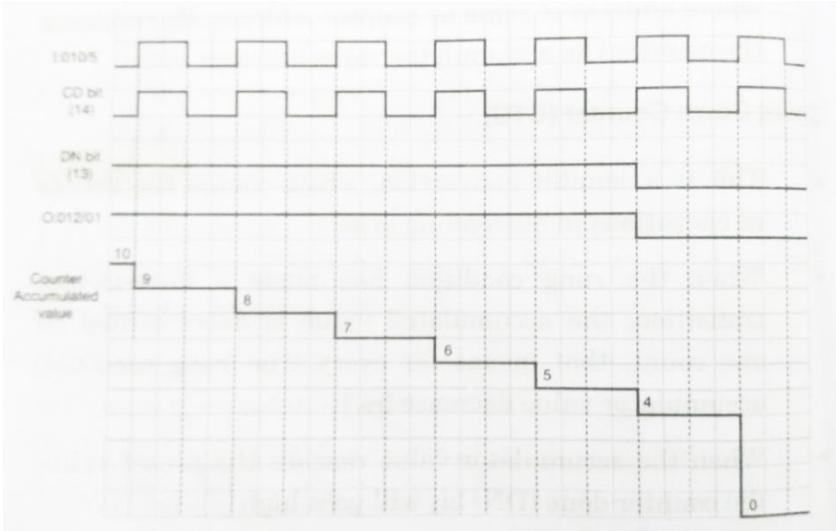


(c)	Differentiate between fixed and modular PLC. (4 points)					04
Ans.		Sr. no	Fixed PLC	Modular PLC	04 Marks (Any four points)	
		1	Elements are fixed on main board of PLC	Elements are modular form, mounted on chasis(rack)		
		2	I/O count is 32 or less than 32	I/O count is more than 32		
		3	Small in size	Size is more		
		4	Easy to install	Complex installation process		
		5	Memory capacity is less	Memory capacity is more		
		6	It can not be repaired	It can be repaired as modules are in modular form		
		7	Generally digital devices are connected to it.	Analog & digital devices are connected to it.		
		8	Cost is less	Cost is more		
		9	Less input output devices are connected	More input output devices are connected		
		10	Application-Tea-coffee vending m/c, Washing m/c	Application-Cement, rubber, Chemical fertilizer industries.		
(d)	List different standard test inputs. Draw them and give their Laplace equations.					04
Ans.	Different standard Test input 1. Step input 2. Ramp input 3. Parabolic input 4. Impulse input				01 Mark list	

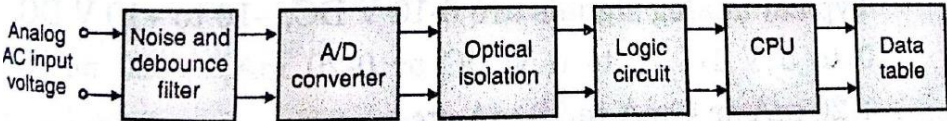


Standard test input	Laplace Representation	Waveforms	03 Marks Laplace equation(Any three inputs)	
Step input(position function) $r(t)$	L.T of $r(t) = R(s) = A/s$			
Ramp input(Velocity function) $r(t)$	L.T of $r(t) = R(s) = A/s^2$			
Parabolic input(Acceleration $r(t)$ function)	L.T of $r(t) = R(s) = A/s^3$			
Impulse input $r(t)$	L.T of $r(t) = R(s) = 1$ if $A=1$			
B)	Attempt any ONE:			06
(a)	Explain with diagram and waveform of down-counter instructions in PLC.			06
Ans.	Functional diagram of Down counter		02 Marks Functional diagram	



	<ul style="list-style-type: none">➤ This is a counter instruction, which counts the events or operations in descending order.➤ When the rung condition has made a false to true transition, the accumulated value is decremented by one count, that means for every true rung condition accumulator value decrease by 1.➤ When accumulator value reaches the preset value, the counter done (DN) bit will go high➤ This DN bit can be used to turn on any output field device. The counter can be reset by using separate input. This input resets the accumulated value to zero  <p>Waveform of down counter</p>	02 Marks explanation	
(b)	List types of control actions. Give its output equation and corresponding laplace transforms.		06
Ans.	<p>Modes of control actions</p> <pre>graph TD A[Modes of control actions] --> B[Discontinuous Controller] A --> C[Continuous Controller] A --> D[Composite Controller] B --> E[ON-OFF controller] C --> F[P] C --> G[I] C --> H[D] D --> I[PI] D --> J[PD] D --> K[PID]</pre>	02 Marks List	



	<p>1) O/P equation of ON-OFF controller</p> $P(t) = 0\% \text{ (OFF)} \quad \text{for } e_p < 0$ $100\% \text{ (ON)} \quad \text{for } e_p > 0$ <p>Where $p(t)$ – Controlled output</p> <p>e_p - Error based on % of span</p> <p>2) O/P equation of PI controller</p> $P(t) = k_p e(t) + k_p k_i \int_0^t e(t) dt + p(0)$ <p>Where $p(0)$ = Initial value of the o/p at $t=0$</p> <p>3) O/P equation of PD controller</p> $P(t) = k_p e(t) + k_p k_d \frac{de(t)}{dt} + p(0)$ <p>4) O/P equation of PID controller</p> $P(t) = k_p e(t) + k_p k_i \int_0^t e(t) dt + k_p k_d \frac{de(t)}{dt} + p(0)$	<p>04 Marks o/p equation</p>	
5	Attempt any TWO:		16
a)	Draw the block diagram of analog input module. Explain each block. List its 4 specification.		08
Ans.	 <pre> graph LR A[Analog AC input voltage] --> B[Noise and debounce filter] B --> C[A/D converter] C --> D[Optical isolation] D --> E[Logic circuit] E --> F[CPU] F --> G[Data table] </pre> <ul style="list-style-type: none"> • Analog input module is a module which connects the PLC to a analog input signal such as signals from thermocouple, flow meter etc. Analog input module give ability to the PLC to monitor continuously time varying signals such as temperature, level, pressure etc. • This module converts the analog signals from analog to digital signal which can be handled by processor. • Typical signal levels are usually 0-10V DC, –10 to +10V DC, 0 to 5V DC, 1 to 5V DC or 0-20mA, -20mA to +20mA or 4mA to 20mA etc. 	<p>03 Marks Block Diagram</p> <p>03 Marks for Explanatio n of each block</p>	



- The block diagram of analog input module consists of filter, ADC, optical isolation, logic circuit. Analog input modules are selected to accept either voltage or current signals.
- When analog input is provided to PLC through analog input module, it reaches different noise and debounce filters. Using these filters the noise is filtered out from the signal
- The signal is converted to digital signal using ADC. This digital signal is passed through optical isolation to logic circuit.
- The logic selection allows digital signal to CPU and on the data table for storage.

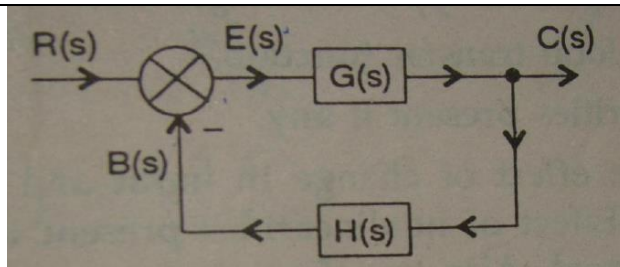
Specifications	Typical Value
Output Voltage	± 10
Output Current	0-20mA
Accuracy (0-55°C)	
Voltage Output	$\pm 2\%$ of Full Scale
Current Output	$\pm 2\%$ of Full Scale
Settling Time	
Voltage Output	100 us
Current Output	2 ms
Maximum Drive	
Voltage Output	5000 Ω
Current Output	500 Ω
Resolution full scale	
Voltage Output	12 bit
Current Output	11 bit

02 Marks
Specifications (Any four)

b) Derive steady state error and error constants equations for Type-0 and Type-1 systems.

08

Ans.



Steady state error can be derived as,

$$E(s) = R(s) - B(s)$$

04 Marks
Derivation of steady state error



But, $B(s) = C(s).H(s)$

$E(s) = R(s) - C(s).H(s)$

But, $C(s) = E(s).G(s)$

So, $E(s) = R(s) - E(s).G(s).H(s)$

i.e. $E(s) [1 + G(s).H(s)] = R(s)$

i.e. $E(s) = \frac{R(s)}{1 + G(s).H(s)}$

Steady state error is given by,

$e_{ss} = \lim_{t \rightarrow \infty} e(t)$

By using final value theorem we get,

$e_{ss} = \lim_{s \rightarrow 0} s.E(s)$

i.e. $e_{ss} = \lim_{s \rightarrow 0} \frac{s.R(s)}{1 + G(s).H(s)}$

K_p , K_v & K_a are obtained by following mathematical equation,

$K_p = \lim_{s \rightarrow 0} G(s).H(s)$

$K_v = \lim_{s \rightarrow 0} s.G(s).H(s)$

$K_a = \lim_{s \rightarrow 0} s^2 G(s).H(s)$

Type of system	Step Input $R(s) = 1/s$	Ramp Input $R(s) = 1/s^2$	Parabolic Input $R(s) = 1/s^3$
0	$\frac{A}{1 + K_p}$	∞	∞
1	0	$\frac{A}{K_v}$	∞

**04 Mark
Error
Constants**

c) Using Routh's criteria, determine the range of K values for system to be stable.

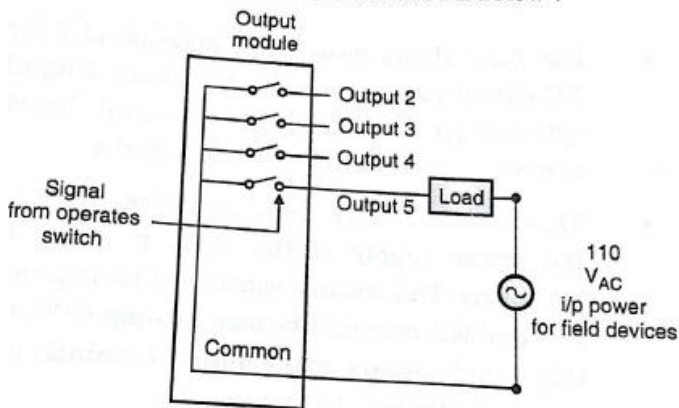
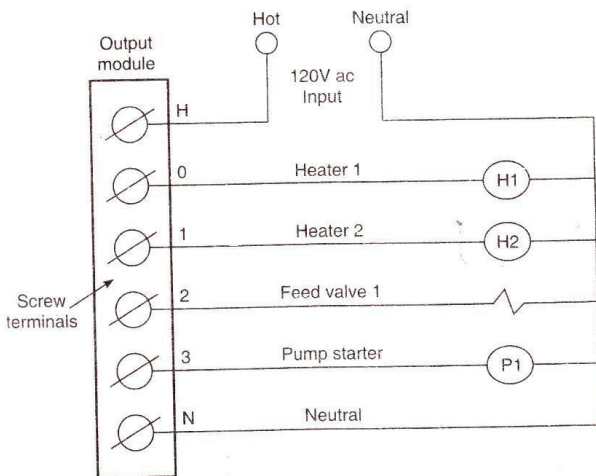
08

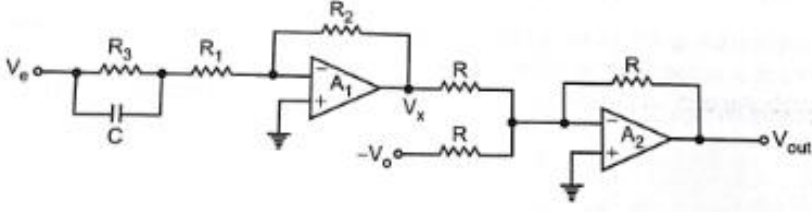
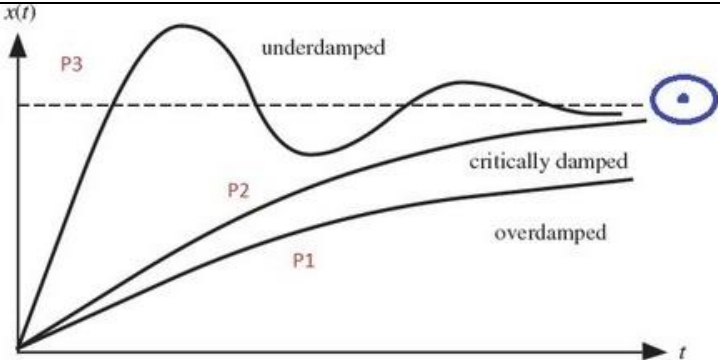


	$G(s).H(s) = \frac{K}{s(s+2)(s+4)(s+5)}$																						
Ans.	<p>The characteristics equation of transfer function is given by $1 + G(s).H(s) = 0$</p> <p>So, $1 + \frac{K}{s(s+2)(s+4)(s+5)} = 0$</p> <p>i.e. $s(s+2)(s+4)(s+5) + K = 0$</p> <p>i.e. $s^4 + 9.s^3 + 20.s^2 + 2.s^3 + 18.s^2 + 40.s + K = 0$</p> <p>i.e. $s^4 + 11.s^3 + 38.s^2 + 40.s + K = 0$</p> <p>thus $a_0 = 1$, $a_1 = 11$, $a_2 = 38$, $a_3 = 40$ and $a_4 = K$</p> <p>The Routh's array can be written as under:</p> <table border="1"> <tr> <td>S^4</td><td>1</td><td>38</td><td>K</td></tr> <tr> <td>S^3</td><td>11</td><td>40</td><td>0</td></tr> <tr> <td>S^2</td><td>$\frac{11 \times 38 - 1 \times 40}{11} = 34.36$</td><td>K</td><td>0</td></tr> <tr> <td>S^1</td><td>$\frac{1374.4 - 11.K}{34.36}$</td><td>0</td><td></td></tr> <tr> <td>S^0</td><td>K</td><td></td><td></td></tr> </table> <p>For stability of a system there should be no change in sign.</p> <p>i) i.e. $\frac{1374.4 - 11K}{34.36} > 0$ and hence $K = 0$</p> <p style="text-align: center;">OR</p> <p>ii) $\frac{1374.4 - 11K}{34.36} > 0$ i.e. $1374.4 - 11K > 0$</p> <p>So, $1374.4 > 11K$</p> <p>$K_{\max} < 124.94$</p> <p>Combining both conditions for stability, we get $0 < K < 124.94$</p>	S^4	1	38	K	S^3	11	40	0	S^2	$\frac{11 \times 38 - 1 \times 40}{11} = 34.36$	K	0	S^1	$\frac{1374.4 - 11.K}{34.36}$	0		S^0	K			<p>02 Marks for characteristics equation</p> <p>04 Marks for Solving Array</p> <p>02 Marks Stability Conditions and range of K</p>	
S^4	1	38	K																				
S^3	11	40	0																				
S^2	$\frac{11 \times 38 - 1 \times 40}{11} = 34.36$	K	0																				
S^1	$\frac{1374.4 - 11.K}{34.36}$	0																					
S^0	K																						
6	Attempt any FOUR of the following		16																				
a)	Explain w.r.t proportional action		04																				



	i) offset ii) proportional band		
Ans.	Offset Error: <ul style="list-style-type: none">• It is a permanent residual error in proportional controller which is inherent in nature.• It is due to one to one correspondence existing between the controller output and error.• A common characteristic of proportional control is an error between the set point and control point, which is referred to as offset or droop.• Offset is an undesirable characteristic of proportional only control loops in proportional controller and is easily eliminated by adding Integral Action. Proportional Band: <ul style="list-style-type: none">• It is a range of deviation, in percent scale; that corresponds to the full range of deviations. It is independent on gain.• It is defined as percentage of error which results in 100% change in controller output.• PB is percentage of full scale change in controller input required to change the controller output from 0% to 100%, corresponding to full operating range of final control element.• Proportional band is defined as the amount of change in the controlled variable required to drive the loop output from 0 to 100%. In a controller the manipulating variable is proportional to the control deviation within the proportional band.• The gain of the controller can be matched to the process by altering the proportional band. If the proportional band is set to zero, the controller action is ineffective.• A very narrow proportional band is tending towards two step control action, since a large change in the controller output will result from a small change in controlled action• A very wide proportional band may result into sluggish or slow control	02 Marks 02 Marks	
b)	State any two advantages and disadvantages of Routh Array.		04
Ans.	Advantages: <ul style="list-style-type: none">• It is a simple algebraic method to determine the stability of closed loop without salving for roots of characteristics equation.• It is very useful for single variable, multivariable and loop systems.• It progresses systematically.• It can determine the range of k for stable operator.• It can judge very easily the relative stability of a system.• It is not tedious or time consuming method.• It helps to determine the conditions of absolute and relative stability	02 Marks each(Any two valid point)	

	<p>of a system.</p> <ul style="list-style-type: none"> It can give the number of roots of the characteristics equation having positive real part in the unstable systems. <p>Disadvantages</p> <ul style="list-style-type: none"> It becomes complex for system of order more than 6 or 7. It cant be applied if coefficients of characteristics equation are complex It is useful to find out only the absolute stability of a system. It is very complex to obtain relative stability of the system It can not tell whether roots are real or complex It cannot give the exact location of the roots. It is valid only if the characteristics equation is algebraic and all coefficients are real. 		
c)	Describe the wiring details of Discrete output module		04
Ans.	 <p>Typical wiring for 120 V AC discrete output module</p>  <p>Typical discrete output module wiring diagram</p> <ul style="list-style-type: none"> The above figure 1 & fig 2 show the basic field wiring for digital 	<p>02 Marks for Diagram</p>	

	<p>120V AC output module. The Wiring diagrams show how wires of output devices are connected to screw terminals of PLC modules. As per the wiring diagram, User has to connect the wires of input and output devices to PLC or Module.</p> <ul style="list-style-type: none"> It can be thought of as a simple switch power can be provided to control the output device. During normal operation, processor sends the output state that was determined by logic diagram of output module. The module then switches the power to the field devices. A fuse is normally provided in that the output circuit of the module to prevent excessive current from damaging the wiring to the field devices. 	02 Mark for relevant Explanation	
d)	Draw electronic PD controller. State its equation and give its two disadvantage.		04
Ans.	<p>Electronic PD Controller</p>  <p>Equation of PD controller can be given as,</p> $P(t) = P(0) + K_p \cdot E(t) + K_p \cdot K_d \cdot \frac{de(t)}{dt}$ <p>Where K_p is proportional controller gain, K_d is derivative controller gain, $e(t)$ is error signal</p> <p>Disadvantages:</p> <ul style="list-style-type: none"> It cannot eliminate the offset of proportional controller. The derivation is assumed to change at constant rate. But if not then it may give unpredictable result. 	<p>02 Marks for Diagram</p> <p>01 Mark For equation</p> <p>½ Mark for Each Disadvantages</p>	
e)	State with diagram the effect of damping on the response of second order system.		04
Ans.			



Effect of damping in response of 2 nd order control system:					01 Mark each for all 4 cases with diagram	
No.	Range of ζ	Type of close loop poles	Nature of response	System Classification		
1	$\zeta = 0$	Purely imaginary	Oscillations with constant amplitude & frequency	Undamped		
2	$0 < \zeta < 1$	Complex Conjugates with negative real parts	Damped Oscillations	Underdamped		
3	$\zeta = 1$	Real, Equal and Negative	Critical & Pure exponential	Critically damped		
4	$1 < \zeta < \infty$	Real, equal & Negative	Purely exponential slow and sluggish	Overdamped		