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WINTER- 18 EXAMINATION

Subject Name: Elements of Electronics <u>Model Answer</u> 17215

Important Instructions to examiners:

- 1) The answers should be examined by key words and not as word-to-word as given in the model answer scheme.
- 2) The model answer and the answer written by candidate may vary but the examiner may try to assess the understanding level of the candidate.
- 3) The language errors such as grammatical, spelling errors should not be given more Importance (Not applicable for subject English and Communication Skills.
- 4) While assessing figures, examiner may give credit for principal components indicated in the figure. The figures drawn by candidate and model answer may vary. The examiner may give credit for any equivalent figure drawn.
- 5) Credits may be given step wise for numerical problems. In some cases, the assumed constant values may vary and there may be some difference in the candidate's answers and model answer.
- 6) In case of some questions credit may be given by judgement on part of examiner of relevant answer based on candidate's understanding.
- 7) For programming language papers, credit may be given to any other program based on equivalent concept.



Question No,Sub Q.No	Answer	Marking Scheme
1	Attempt any TEN	: 20
a)	Give the detail classification of capacitor. Capacitors Fixed Variable Electrostatic Electrolytic Aluminium Tantalum Ans: Ans:	(Classification- 2M)
b)	Write two applications of i) LED ii) photodiode. Ans:- LED: (Any Two) 1. In the opto- couplers. 2. In the infrared remote controls. 3. As indicators in various electronic circuits. 4. In seven segment and alphanumeric displays. 5. In optical switching applications. 6. For image sensing circuits in picture phone. 7. In burglar alarm system. Photodiode: (Any Two) 1. In the cameras for sensing the light intensity. 2. In CD players. 3. In the fiber optic receiver. 4. As photo (light) detector. 5. In light intensity meters. 6. In the object counting system.	(1M Each)

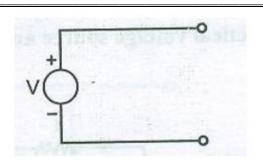


	(ISO/IEC - 2/001 - 2013 Certified)	
c)	State maximum power transfer theorem. Ans:	(Statement-2m
	The maximum power transfer theorem states that the maximum amount of power will be delivered to the load resistance when the load resistance is equal to the Thevenin / Norton resistance of the network supplying the power. If the load resistance is lower or higher than the Thevenin/ Norton resistance of the source network, then the power delivered to load is less than maximum. That means the condition for maximum power transfer according to maximum power transfer theorem is, $R_{L} = R_{TH}$	
d)	Define active and passive network.	(1M Each)
	Active network: If a network consists of an energy source, then it is called as an active network. The type of energy source can be voltage source or a current source. Passive network: If a network does not contain any energy source then it is called as the passive network.	
e)	List four specifications of resistor.	(Any Four specifications
	Ans:	2M)
	 Specifications of resistor: Temperature Coefficient. Size or value of a resistor Power Dissipation / wattage Tolerance Thermal Stability Frequency Response. Power De-rating. Maximum Temperature. Maximum Voltage. 	
	iviaximam votage.	
f)	Draw ideal and practical voltage source and current source.	(1/2 M Each)
	Ans: Voltage Source:	

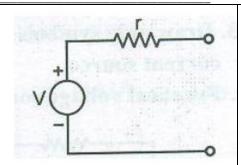


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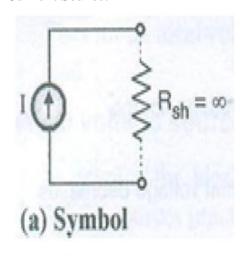


Ideal Voltage Source

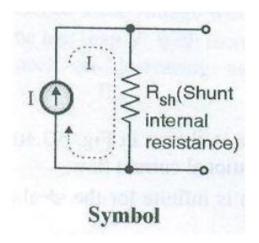


Practical Voltage Source

Current Source:



Ideal current source



Practical current source

(1M Each)

g List two applications of i) clipper ii) clamper.

Ans: Clipper: (Any Two)

- 1. Diode clamp
- 2. wave shaping circuits
- 3. Clippers can be used as voltage limiters and amplitude selectors.

Clamper: (Any Two)

- 1. Voltage Multipliers
- 2. For the protection of the amplifiers from large errant signals clampers are used.
- 3. Clampers can be used for removing the distortions



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	4. For improving the overdrive recovery time clampers are used.	
h	Draw the symbol of i) Iron core inductor ii) variable capacitor iii) resistor iv) Ferrite core inductor	:(1/2M Each)
	Ans Iron core inductor:	
	$\overline{\infty}$	
	Variable capacitor:	
	(ii) variable capacitor	
	(ii) variable enpaction	
	Resistor:	
	1	
	 	
	Fixed Value	
	Resistor	
	Ferrite core inductor:	
	===	
	000	
i	State the need of filters.	(Need-2m)
	Ans: The output of a rectifier is pulsating D.C. [i.e. it contain A.C along with D.C components]. The	
	A.C. components are undesirable and must be removed from the pulsating D.C to obtain pure D.C. signal. To remove the ripples, filter circuit is used.	
		(1)(5)
j	Define ferromagnetic and ferrimagnetic materials.	(1M Each)
	Ans: Ferromagnetic materials: The materials which possess magnetism in the absence of applied	



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	magnetic field is known as ferromagnetic materials.	
	Ferrimagnetic materials: Ferrimagnetic materials are like ferromagnets in that they hold a spontaneous magnetization below the Curie temperature and show no magnetic order (are paramagnetic) above this temperature.	
k	State kirchoff 's current and voltage law (KCL & KVL)	: (1M Each)
	Ans Kirchoff's Current law (KCL): The algebraic sum of all currents entering or leaving a node must be equal to zero .Therefore,	(1112 21101)
	$\Sigma I = 0$	
	Kirchoff's Voltage law (KVL): It states that "Algebraic sum of voltages in a loop or mesh is equal to zero". That is Σ , Σ voltage= 0	
1	Define self and mutual induced emf.	(1M Each)
	Ans:	,
	Self-induced emf: Self-induced emf is the e.m.f induced in the coil due to the change of flux produced by linking it with its own turns.	
	Mutual induced emf: The emf induced in a coil due to the change of flux produced by another	
	neighbouring coil linking to it, is called Mutually Induced emf.	
2	Attempt any FOUR:	16
_		
а	Draw negative clamper circuit and explain its working.	: (Diagram-
	Ans Diagram:	2M, Working-
	C C	2M)
	V_{m} V_{m} V_{i} V_{i} V_{i} V_{i} V_{o} V_{o} V_{o} V_{o}	
	-V _m DC level	
	Output waveform Negative clamper	
	Working:	

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During the positive half cycle of the input AC signal, the diode is forward biased and hence no signal appears at the output. In forward biased condition, the diode allows electric current through it. This current will flows to the capacitor and charges it to the peak value of input voltage in inverse polarity $-V_m$. As input current or voltage decreases after attaining its maximum value V_m , the capacitor holds the charge until the diode remains forward biased.

During the negative half cycle of the input AC signal, the diode is reverse biased and hence the signal appears at the output. In reverse biased condition, the diode does not allow electric current through it. So the input current directly flows towards the output.

When the negative half cycle begins, the diode is in the non-conducting state and the charge stored in the capacitor is discharged (released). Therefore, the voltage appeared at the output is equal to the sum of the voltage stored in the capacitor $(-V_m)$ and the input voltage $(-V_m)$ {I.e. $V_o = -V_m - V_m = -2V_m$ } which have the same polarity with each other. As a result, the signal shifted downwards.

b Draw Thevenin's equivalent circuit for the circuit shown in fig.2(b)

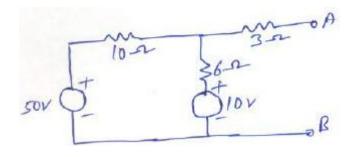


Fig.2(b)

Ans

Ans:-50V 100P 100P 36D 36D 36D 36D 36D 70V 70V

Page **7** of **29**

:(Calculate VTh-2M, Calculate RTh-1M, Equivalent Circuit-1M)

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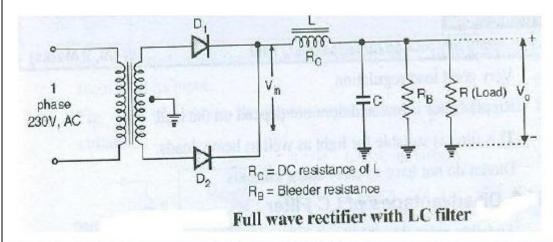
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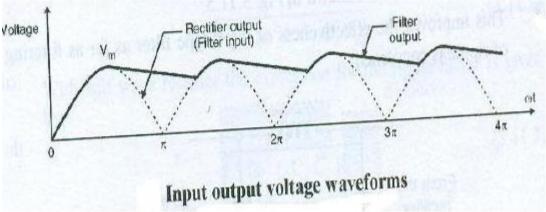
Draw and describe circuit diagram of centre tapped rectifier with LC filter along with its input and output waveforms.

(Circuit diagram-2M, Explanation-1M, Waveforms-1M)

Ans:

С





Explanation:

A centre- tap full wave rectifier drive produces the rectified voltage and it drives the LC filter. We know that the series inductor filter is preferred for low values of load resistance (i.e. heavy loads) and the shunt capacitor filter is preferred for high values of load resistance (i.e. light loads) Therefore, the LC filter can give low ripple factor irrespective of the load current as it is combination of these two filters.

The series connected inductor offers a high reactance to the harmonic components (ripple) in the output and attenuate them. The parallel capacitor C now provides a low reactance by- pass for the ripple and these ripples are grounded. Thus, this reduces the ripple further because they are not allowed to pass through the load RL. The output voltage waveforms are similar to that as shown in figure above.



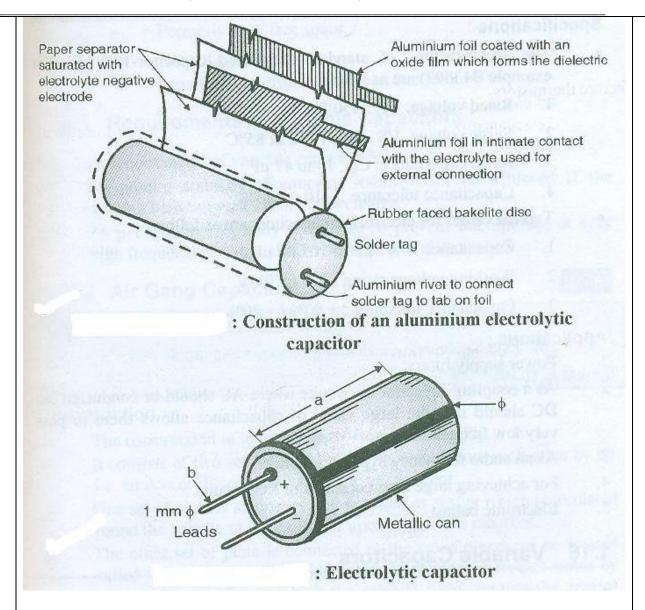
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d	Find resistance value	for the given colour codes :	(1M Each)
	i)	Red Brown Black Gold	
	ii)	Blue Yellow Grey	
	iii)	Blue Violet Red Silver	
	iv)	Red Red Orange Gold	
	Ans:	Red Brown Black Gold	
	$R=21*10^{0}, \pm 5\%$		
	=21Ω, ±5%		
	i)	Blue Yellow Grey	
	$R=64*10^8, \pm 20\%$		
	$=6.4G\Omega, \pm 20\%$		
	ii)	Blue Violet Red Silver	
	R=67*10 ² , ±10%		
	$=6.7$ K Ω , $\pm 10\%$		
	iii)	Red Red Orange Gold	
	R=22*103, ±5%		
	=22KΩ, ±5%		
е	Compare half and full	wave bridge rectifier on the basis of :	(1M Each)
	i)	DC output voltage	
	ii)	Ripple factor	
	iii)	Efficiency	
	iv)	rms value of voltage	
		C C C C C C C C C C C C C C C C C C C	
	Ans:		



		Full wave Bridge Rectifier	
DC output voltage	Vm/π	2Vm/π	-
Ripple factor	1.21	0.482	
Efficiency	40.6%	81.2%	
rms value of voltage	Vm/2	Vm/√2	_

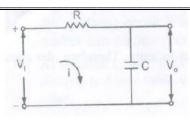
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- Electrolytic capacitor is a polarized capacitor. This type of capacitor consists of pasty, semiliquid electrolyte between aluminum foil electrodes or plates.
- An electrolytic capacitor is a type of capacitor typically with a large capacitance per unit volume than other types than other types, making them useful in high current and low frequency electrical circuits.
- Electrolytic capacitors have highest capacitance voltage (CV) product in a given case size as well as the largest capacitance value. The large capacitance values are attainable because of the use of a very thin dielectric film formed by oxidizing a metal usually aluminum tantalum.
- There are two types of electrolytic capacitors:
 - 1. Aluminum electrolytic capacitor.
 - 2. Tantalum electrolytic capacitor.
- A plain foil dry electrolytic capacitor is made by forming a coating of aluminium oxide on both sides of an aluminium foil.
- Two stripes of aluminium foil used are then separated by two layers of porous paper soaked with electrolyte. This assembly is rolled up, the ends closed with wax and then sealed into an aluminium container as shown in fig.

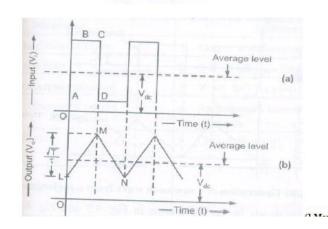


3	Attempt any four of the following	
a)	Draw BH Curve and define coercivity and reluctivity.	2M for
	Ans:	Diagram &
	DVI C	1M each for
	BH Curve:	correct
	Flux Density B +Bmax C Saturation Residual Magnetism D A Coercive force H C Bres Bres Positive magnetizing current Negative magnetizing current	definitions
	Definitions: Coercivity: For ferromagnetic material the coercivity is the intensity of the applied magnetic field required to reduce the magnetization of that material to zero after the magnetization of the sample has been driven to saturation. Thus coercivity measures the resistance of a ferromagnetic material to becoming demagnetized.	
	Reluctivity: It is equal to the ratio of intensity of the magnetic field to the magnetic induction of the material.	
b)	Draw and describe working of RC integrator with output waveform for square wave	2M for RC
	input.	integrator
	Ans:	circuit
		diagram.1M
		for working &
		1M for output
		waveform



 $V_o \propto \int V_i \cdot dt$

(2 Marks) (1 Marks)



Mathematical Analysis:-

Vi : ac input voltage.

i : Resulting alternating current.

q : Charge on the capacitor at any instant.

Since R is very large as compared to $X_{\mathbb{C}}$, therefore voltage across R i.e. $V_{\mathbb{R}}$ is equal to the input voltage

$$V_i = V_R$$

$$i = \frac{v_R}{R} = \frac{v_i}{R}$$

The charge q on the capacitor at any instant.

$$q = \int i.dt$$

output voltage $V_O = q_-$

$$=\int_{0}^{1} \frac{dt}{t}$$

$$=\frac{1}{PC}$$

Vo
$$\alpha \int V_i . dt$$

Hence, output voltage directly proportional to integration of input voltage.

c) Compare C,L,LC & π filter (any 4 points) Ans:

1M each for correct



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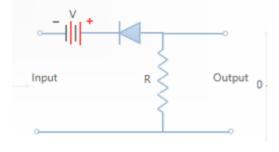
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Parameter	C filter	L filter	LC filter	π filter
1.Components	Only	Only	One C &	Two C &
used	capacitor	capacitor	One L	one L
	C	L		
2.Place of	Across the	In series	Across the	Across
filter	load	with load	load	the load
3.Output DC voltage	Highest	Lowest	High	Higher
4.Ripple content	Low	High	Low	Lowest
5.Ripple factor	Low	High	Lower	Lowest
6.Load	Poor	Moderate	Good	Very
regulation				Good
7.Applications	Cell	High	DC power	DC power
	charger,	current	supplies	supplies
	small	DC power		
	eliminators	supplies		
	etc			

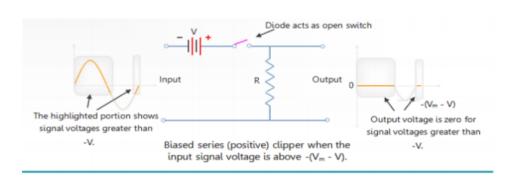
d) Draw and describe working of series biased clipper with input output waveforms.
Ans:

Biased series positive clipper:

Clipping of the signal takes place as soon as the input signal goes positive. If we want to change/adjust the clipping level of AC voltage, then external biasing voltage must be used. The figure given below shows a biased (series) clipper



Biased series (positive) clipper

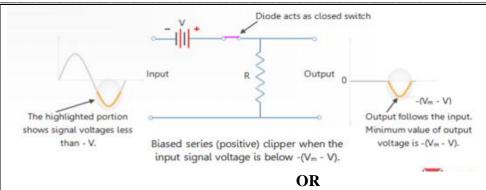


1M for diagram & 2M for explanation & 1M for waveform (Either Biased series positive or Negative clipper)



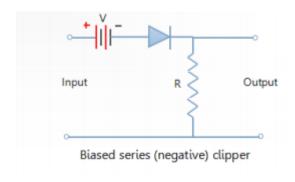
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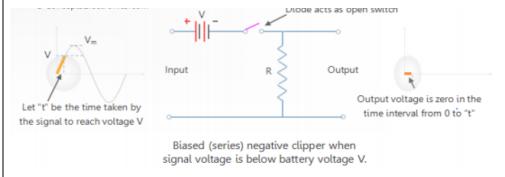
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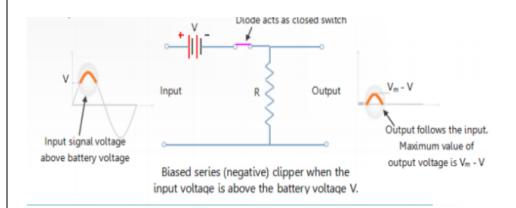


Biased Series negative Clipper:

Clipping of the signal takes place as soon as the input signal goes negative. If we want to change/adjust the clipping level of AC voltage, then external biasing voltage must be used. The figure given below shows a biased (series) clipper.







Calculate I_L for the network shown in fig.

e)

calculation of I_L & I_L 1M each &



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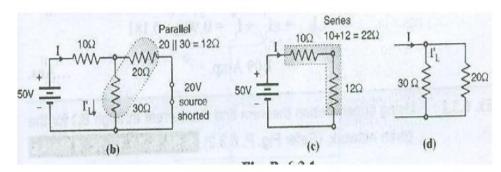
20Ω 10Ω ≥30Ω

Calculation of $I_L 2M$

Ans:

Ans. (I'L & I'L - 2 Marks each)

Step 1: Current through 30Ω resistor due to only 50V source (I'L)



$$R_T$$
 = 10 + 12 = 22 Ω
∴ Total current I = $\frac{50 \text{ V}}{R_T} = \frac{50}{22} = 2.27 \text{ Amp.}$
 $I'_L = \frac{20}{20 + 30} \times I$

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Current division between parallel resistors

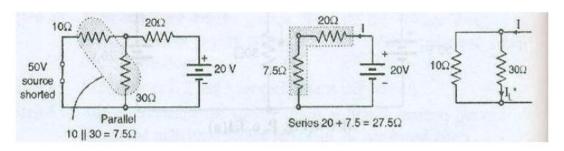
Step 2: Current through 30Ω resistor due to only 20V source (I''L):

Now assume the 50V source to be shorted and obtain the effect of 20 V source only.

$$R_{\rm T} = 27.5 \,\Omega$$

$$\therefore I = 20 \,\text{V} / 27.5 \,\Omega = 0.73 \,\text{Amp.}$$

$$I''_{\rm L} = \frac{10}{10 + 30} \times I = \frac{1}{4} \times 0.73 = 0.181 \,\text{A}$$



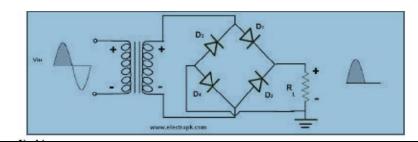
Step 3: Calculate the total current through 30Ω resistor (I_L):

As per the superposition theorem, the total current through the 30Ω resistance is equal to the algebraic sum of the currents due to individual sources($I'_{L \text{ and }}I''_{L}$)

Therefore,

$$I_{L} = I'_{L} + I''_{L} = 0.909 + 0.181 = 1.09 \text{ Amp} \dots Ans$$

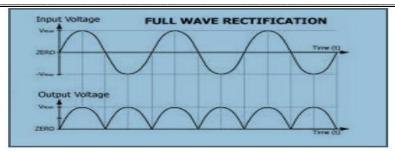
f) Draw & describe working of bridge rectifier with its input output waveforms
Ans:



1M each for circuit diagram & waveforms & 2M for explanation

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Four diodes are used.

- a)In positive half cycle Diodes D1 & D3 becomes forward biased they will conduct & current flows at that time D2 and D4 are reverse biased they will not conduct.
- b) In negative half cycle diode D2 and D4 becomes forward biased they will conduct ,current flows in same direction at that time D1 and D3 are reverse biased they will not conduct. we get full cycle rectification.

4 a) Compare PN junction diode with zener diode (any four points) Ans:

1M each for correct comparison point

Sr.No.	PN Junction Diode	Zener Diode
1	It is not properly doped to control reverse breakdown.	It is properly doped to control reverse breakdown.
2	It conducts only in one direction.	It conducts in both directions.
3	It is always operated in forward-bias condition.	It is always operated in reverse-bias condition.
4	It has no sharp reverse breakdown.	It has quite sharp reverse breakdown.
5	It burns immediately, if applied voltage exceeds the breakdown voltage.	It will not burn, but functions properly in breakdown region.
6	It is commonly used for rectification purpose.	It cannot be used for rectification, but commonly used for voltage regulation.

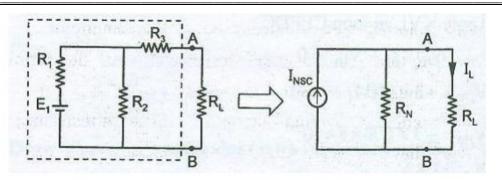
b) State Norton's theorem with suitable example Ans:

Statement:- "Any linear, active, resistive, network containing one or more voltage and / or current sources can be replaced by an equivalent circuit containing a current source called Norton's equivalent current ISC and an equivalent resistance in parallel."

2M for statement & 2M for example

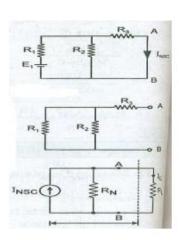
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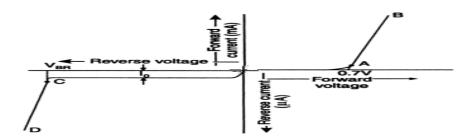
Explanation

- 1. Remove R_L
- Short Terminals A&B.
- Calculate flowing through A &B when short circuited.
 This current is I_{NSC}
- To calculate R_N replace all energy sources by their internal resistance if any and if not then voltage source is replaced by short circuit whereas current source is replaces by open circuit w.r.t terminal A&B.
- 5. Draw the Norton's Equivalent.



c) Draw VI characteristics of PN junction diode and define knee voltage, leakage current,reverse saturation current,breakdown voltage Ans:

V - I characteristics of P - N junction diode



Knee voltage:

The applied forward voltage at which the PN junctions start conducting is called the cut-in voltage. It is also known as knee voltage (Vk or Vz). The value of cut-in voltage is 0.7 V for Silicon and 0.3 V for Germanium PN junction diodes.

Leakage current:

When a **diode** is reverse biased, the width of the depletion region increases. This **current** is called **leakage current**. **Leakage current** is dependent on minority **current** carriers.

2M for characteristics & ½ mark each for correct definitions

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Reverse saturation current:

In reverse bias condition there will be negligible amount of current that will flow through the device due to minority carrier which is called as reverse saturation current.

Breakdown voltage:

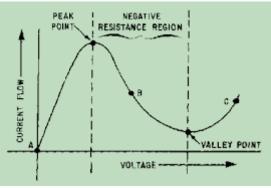
The voltage at which the abrupt increase in reverse current through the PN junction occurs is known as breakdown voltage.

d) Draw VI characteristics of Tunnel diode & describe it.
Ans:

2 M for Diagram & 2M for

Explanation

Tunnel diode characteristics

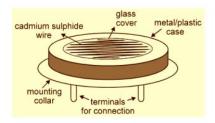


- a)It is also capable for very fast operations.
- b)It has heavily doped PN junction of only 10 nm wide .
- c)It exhibit negative resistance region.
- d) Applications:microwave applications, ultra high speed switching device,relaxation oscillators.
- e)The process of penetrating charge carrier directly through potential barrier is called tunneling.

e) Draw & describe construction of LDR Ans:

Constructional diagram of light dependent resistor (LDR):

2 M for Diagram & 2M for Explanation



Constructional diagram of LDR

Description:

- 1. Photo resistors (or LDR) are manufactured from photo-conductive semiconductor materials such as cadmium sulphide (CdS), cadmium selenide (CdSe) and lead sulphide (PbS).
- 2. These resistors have a ceramic substrate, over which a layer of cadmium sulphide (CdS) is deposited in zig-zag form to increase the length. This increases the resistance value.
- 3. Depending upon the thickness, surface area and length of the layer, the resistance value changes. The electrodes are formed by evaporating metal in vacuum.
- 4. The leads are connected and put in plastic case. These are available in the form of discs with wire lead ends on one side.
- 5. The resistance of photo resistors may be several mega ohms in total darkness and less



	than 100Ω , when well illu					
f)	Define i)Linear network network Ans:	ii)Non-linear network iii)l	Bilateral network iv)Unilateral	1M each for correct definitions		
	Linear Network:					
	If the characteristics, parameter such as resistances, capacitances, inductances etc. remain constant irrespective of changes in temperature, time, voltage etc. then the circuit or network is called Linear network.					
	Non-Linear Network: If the parameters of a network change their values with change in voltage, temperature, time etc. then the network is called as Non-Linear Network					
	Bilateral Network: If the magnitude of current flowing through a circuit element is not affected when the polarity of the applied voltage is changed, then the element is known as a bilateral element. The bi lateral elements offer the same impedance in both directions. A resistors is a bilateral element. A network comprising of one or more number of bilateral elements is known as a bilateral network.					
		onse or behavior of a network	is dependent on the direction of alled as a unilateral network.			
Q5.	Attempt any Four			16		
(a)	Compare clipper and clamp	er circuit(any four points.)				
	Ans:- Sr. No Parameter	Clipper	Clamper	(Any four relevant points – 1		
	S1. 140 I drameter	Спррсі	Clamper	Mark each)		
	1 Components used	Diode, Resistors	Diode, Capacitors, Resistors	,		
	2 Function	To remove a part of input waveform	To add a DC shift to the input waveform			
	3 Frequency of inpu	tNot important as capacitor is not used	The value of C needs to be chosen on the basis of input frequency			
	4 Application	Diode clamp, wave shapin circuits	gVoltage Multipliers			
(b)	Describe the working prin	nciple of LED with neat const	ruction.	(Working figure – 2 Mark, Explanation – 2 Marks)		



	Working Diagram Working:-When the junction is forward – biased the electron in the n-region combines with the holes. • These free electrons reside in the conduction band and at the higher energy level from the holes in the valence band. When the recombination takes place, these electrons return back to the valence band which is at a lower energy level than the conduction band.	
	While returning back, the recombining electrons give away the excess energy in the form of light	
c)	 (i) Rectifier: A rectifier may be defined as an electronic device, such as a PN junction diode, used for converting alternating (AC) voltage or current into unidirectional (DC) voltage or current. (ii) PIV of diode: Peak Inverse Voltage (PIV) is defined as the maximum reverse/negative voltage which appears across non-conducting reverse biased diode without damaging it. (iii) Ripple factor: Ripple Factor is defined as the ratio of RMS value of the AC component of output to the DC or average value of the output. Mathematically it is expressed as, 	Each definition- 1 mks
	Ripple Factor, Y = RMS value of AC component present in Rectifier Output Average Value of Rectifier Output	
	(iv) Rectifier efficiency: Defined as the ratio of DC output power to the AC input power.	
	η = DC output power/ AC input power = PLdc / Pac	

(Autonomous)

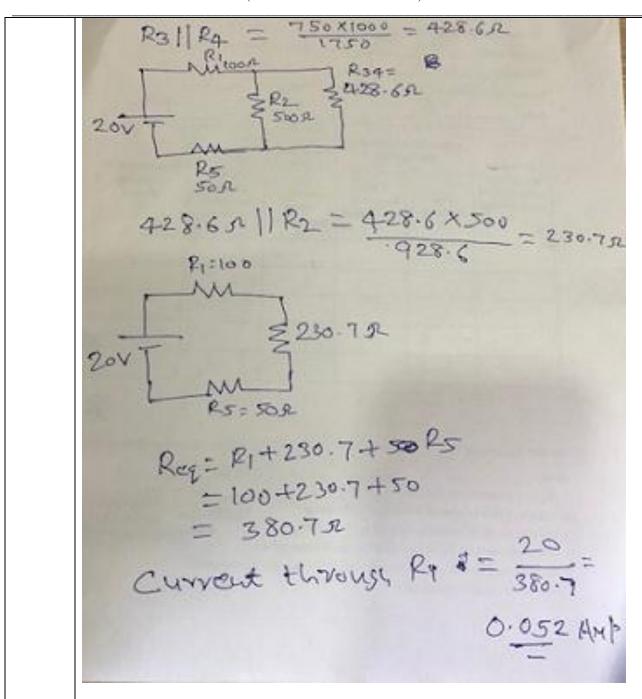
(ISO/IEC - 27001 - 2013 Certified)

soln.: d) Proper solution with answer- 2 Secondary voltage (rms) = $V_S = \frac{230}{10} = 23 \text{ V}$ mks each DC output voltage $V_{Ldc} = \frac{V_m}{\pi} = \frac{23\sqrt{2}}{\pi} = 10.35 \text{ V}$ PIV of diode = $V_m = \sqrt{2} V_S = 23 \sqrt{2} = 32.52 V$ Find current through R1 in following circuit: (e) 4 marks $R_1 = 100 \Omega$ $\begin{cases} R_3 = \\ 750 \Omega \end{cases}$ $R_2 =$ $R_4 = 1000 \Omega$ 500 Ω $R_5 = 50 \Omega$



(Autonomous)

(ISO/IEC - 27001 - 2013 Certified)



(f) List types of TDR and compare them.(any two) Types of TDR: 2 mks, comparison-2 Proper solution- 4 mks mks 1. NTC- Negative temperature of coefficient of resistance 2. PTC- Positive temperature of coefficient of resistance PTC NTC Materials used Barium titanate, titanium oxide, Manganese, nickel, cobalt, powdered barium carbonate titanium



(Autonomous)

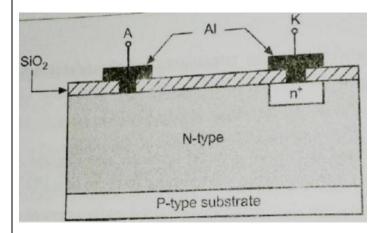
(ISO/IEC - 27001 - 2013 Certified)

	T	
Characteristics		1
		t i
	1 /	1 \
	/ lauce	ance
	Resistance	Resistance
	Temperature	Temperature
Temperature	60°C to 180°C	+200°C to +1000°C
coefficient		
Application (any 1)	1) Temperature sensing in	1) For temperature
	electric motors and	measurement and control
	transformers for protection.	2) Temperature
		compensation
	2) Liquid level sensor	
		3) Fluid flow
	3) To protect solid state fuse	measurement
	against excess current	

6 Attempt any FOUR 16

2 mks Diagram , 2marks working

Describe working of schottky diode with neat construction



Working

а

- The metal region of a Schottky diode is heavily occupied with the conduction band electrons and the N-type region is lightly doped.
- There are no minority carriers as in other types of diodes, but there are only majority carriers as electrons. It operates only with majority carriers.
- When it is forward biased, higher energy electrons in the N regions are injected into the metal region where that gives up their excess energy very rapidly.
- Since there are no minority carriers as in conventional diodes, there is no charge storage and



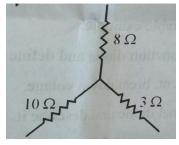
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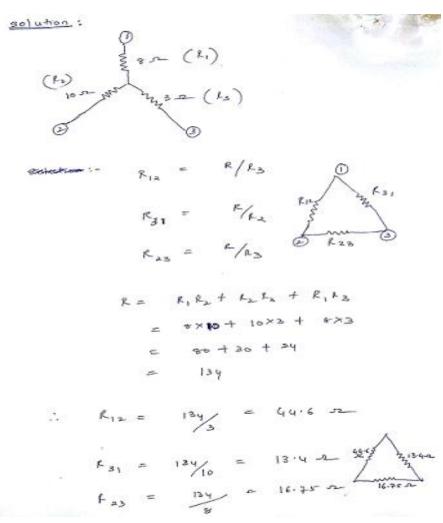
hence there is no reverse recovery diode when it is switched from the forward-biased condition (i.e. ON state) to the reverse biased condition (i.e. OFF state).

It has negligible storage time and hence there is a very rapid response to a change in bias. Because of this property, it acts as a very fast switching diode.

b) Convert the given star network into equivalent delta network.



Proper conversion 4 mks





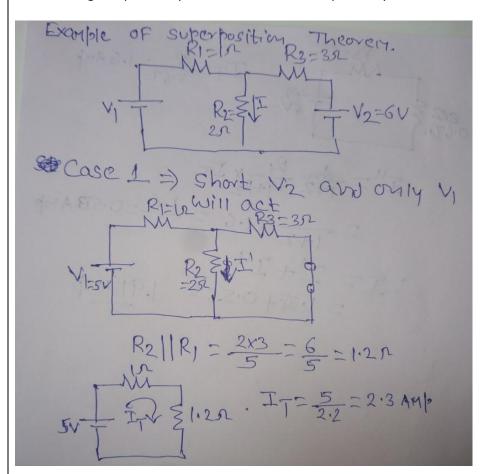
c)	Compare RC integrator with RC differentiator(ar	Any 4 points – 1 mks each	
			1 mms cash
	Sr. RC Integrator No	RC Differentiator	
	1 Output is integration of input	Output is differentiation of input	
	2 For proper operation T<< RC	For proper operation T>> RC	
	3 Output decreases as frequency is increased	Output increases as frequency is increased	
	4 It is basically a low pass filter	It is high pass filter	
	5 Configuration Vi i C Vo	Configuration Vi Vi Vo Vi = Input voltage	
		V _o = Output voltage	
d	Describe working principle of LASER. Positive Terminal of the Battery Highly Reflective Surface P-type Intrinsic Active Region Negative Terminal of the Battery	Diagram – 2 mks, principle - 2 mks	
	There are three main processes in semiconductors 1. Light absorption 2. Spontaneous emission		
	an electron and release energy in the fo	is new photon of identical wavelength and phase. In	
	 This type of process is the basic principle Photon, with energy equal to E2 – E1 int to return to lower energy state with the Second photon has the same phase, free 	e on which LASER Diode operates. teracts with an atom in upper energy state, causing it e emission of a second photon.	
е	State superposition theorem with suitable example Superposition theorem states that: In a linear circuit with several sources the voltage and current		Statement – 2 mks, example- 2 mks
	responses in any branch is the algebraic sum of	_ 11110	

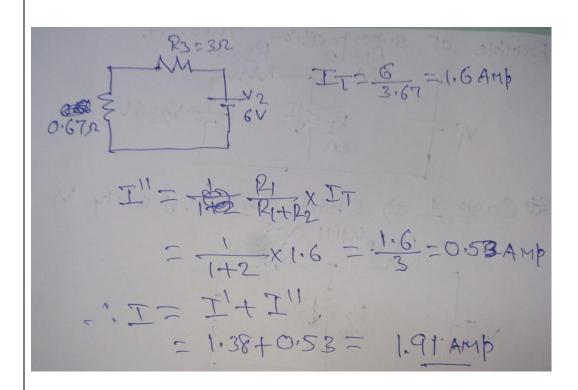


(Autonomous)

(ISO/IEC - 27001 - 2013 Certified)

source acting independently with all other sources replaced by their internal impedance.







(Autonomous) (ISO/IEC - 27001 - 2013 Certified)

f) Draw and describe the VI characteristics of zener diode 2 mks Characteristics 2marks explanation Breakdown (or regulation region) Reverse characteristic of a zener diode. A zener diode is operated only in the reverse-bias region. (generally written as VR) is increased, the reverse current (usually called zener current, IZ) remains negligibly small up to the 'knee' of the curve point. At this point, the effect of breakdown process begins. From the bottom of the knee, the breakdown voltage (also called zener breakdown voltage or simply zener voltage VZ) remains essentially constant. This ability of a diode is called regulating ability and is an important feature of a zener diode. It maintains, an essentially a constant voltage across its terminals over a specified range of zener current values. Following two points are important from the characteristics of a zener diode: 1. There is a minimum value of zener current called breakover current designate a IZX or IZ(min) which must be maintained in order to keep the diode in breakdown (or regulation) region. When the current is reduced below the knee of the curve, the voltage changes drastically and the regulation is lost. 2. There is a maximum value of zener current designated as IZM or IZ(max) above which the diode may be damaged. The value of this current is given by the maximum power dissipation of the zener diode. As long as the maximum power dissipation is not exceeded, the diode will not be damaged. It will come out of the breakdown region, when the applied reverse voltage is reduced below the breakdown voltage.